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Neil H. E. Weste (born 1951), is an Australian inventor and engineer, noted for having designed a 2-chip wireless LAN implementation and for authoring the textbook Principles of CMOS VLSI Design. He has worked in many aspects of integrated-circuit design and was a co-founder of Radiata Communications.

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Details techniques for the design of complex and high performance CMOS Systems-on-Chip. This edition explains practices of chip design, covering transistor operation, CMOS gate design, fabrication, and layout, at level accessible to anyone with an elementary knowledge of digital electronics.

With this revision, Weste conveys an understanding of CMOS technology, circuit design, layout, and system design sufficient to the designer. The book deals with the technology down to the layout level of detail, thereby providing a bridge from a circuit to a form that may be fabricated.

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of the entire field of modern CMOS VLSI Design. The authors draw upon extensive industry and classroom experience to introduce today's most advanced and effective chip design practices. They present extensively updated coverage of every key element of VLSI design, and illuminate the latest design challenges with 65 nm process examples. This book contains unsurpassed circuit-level coverage, as well as a rich set of problems and worked examples that provide deep practical insight to readers at all levels.

This is an up-to-date treatment of the analysis and design of CMOS integrated digital logic circuits. The self-contained book covers all of the important digital circuit design styles found in modern CMOS chips, emphasizing solving design problems using the various logic styles available in CMOS.

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